

FIG. 1

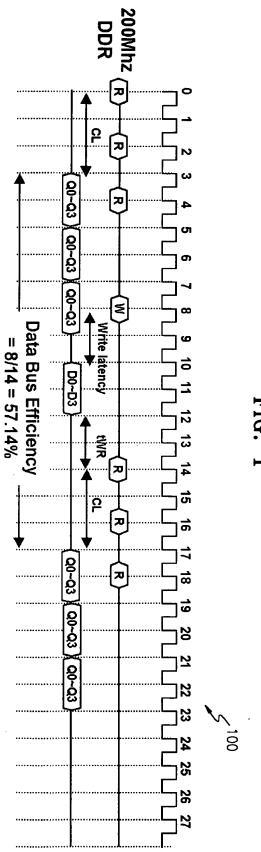
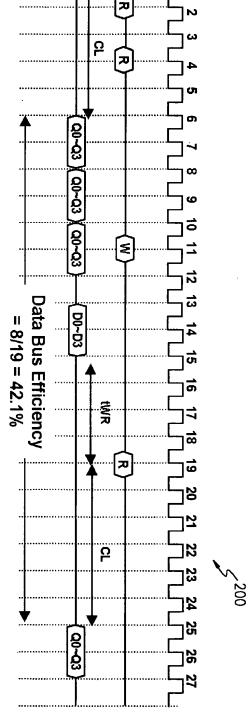


FIG. 2



400Mhz DDR



FIG. 3

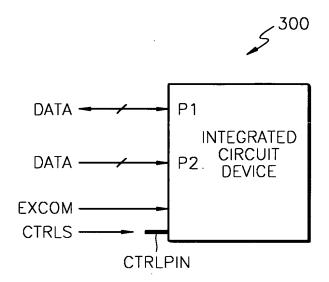
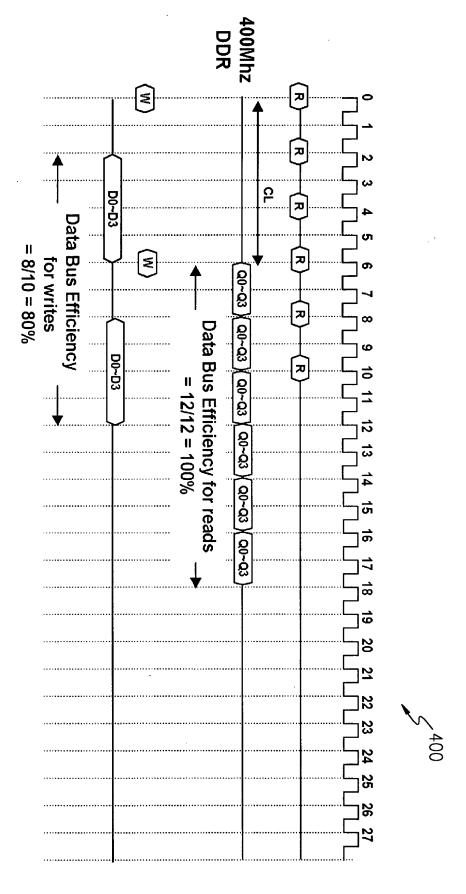




FIG. 4





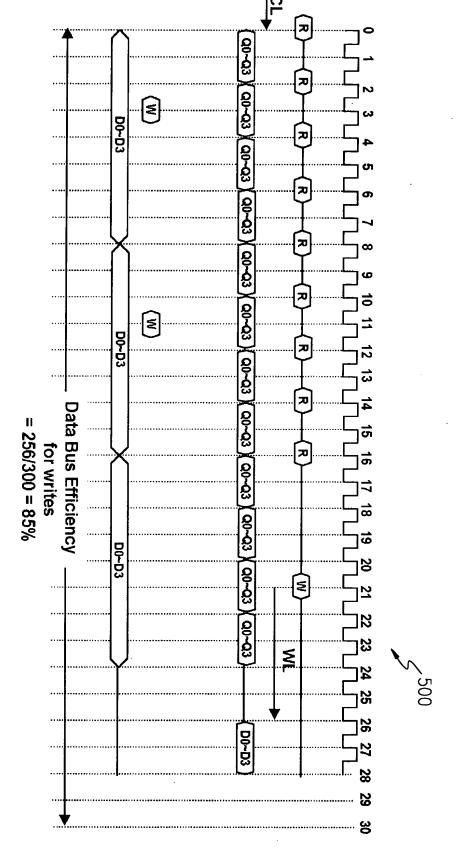
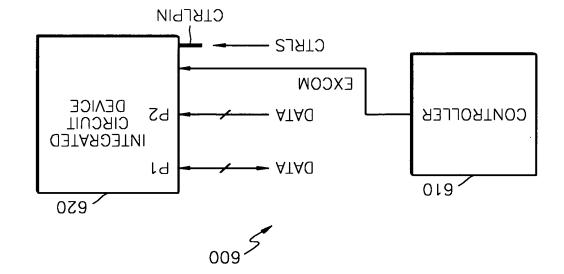
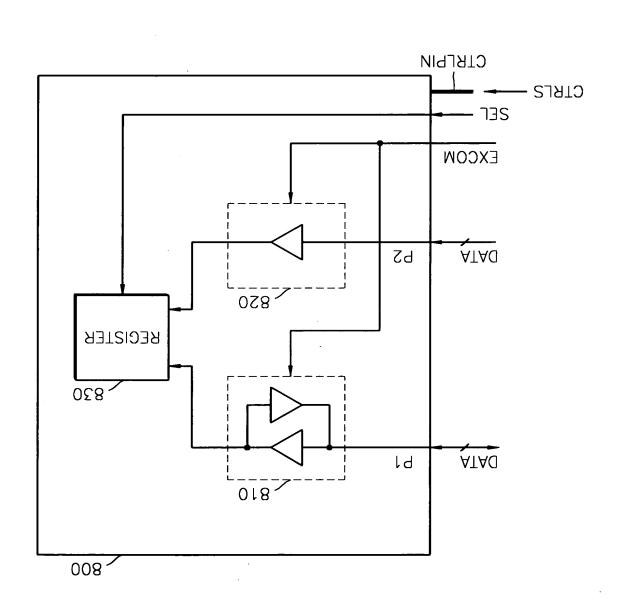


FIG. 6







EIC. 8

